

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): M. Kapur et al.  
Docket No.: YOR920030306US1  
Serial No.: 10/650,222  
Filing Date: August 28, 2003  
Group: 2138  
Examiner: Saqib Javaid Siddiqui

Title: Self-Synchronizing Pseudorandom  
Bit Sequence Checker

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**TRANSMITTAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

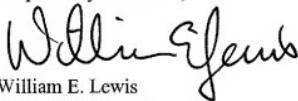
Sir:

A Notice of Appeal was submitted on June 21, 2007 to reinstate the appeal. Applicants will file their Appeal Brief within the statutory time period for filing an Appeal Brief after submission of a Notice of Appeal.

In the Office Action dated March 21, 2007 for the above-identified application, the Examiner objected to FIGS. 1-3. Replacement sheets for FIGS. 1-3 are submitted herewith. The replacement sheets designate FIGS. 1-3 as prior art, as per the Examiner's request.

It is believed that no additional fees are due, but in the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **International Business Machines Corporation Deposit Account No. 50-0510** as required to correct the error.

Respectfully submitted,



William E. Lewis  
Attorney for Appellant(s)  
Reg. No. 39,274  
Ryan, Mason & Lewis, LLP  
90 Forest Avenue  
Locust Valley, NY 11560  
(516) 759-2946

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